

### Design of a USB Device Driver

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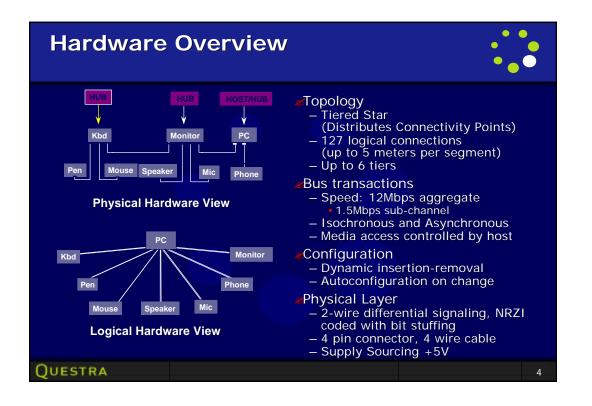
### **Outline**

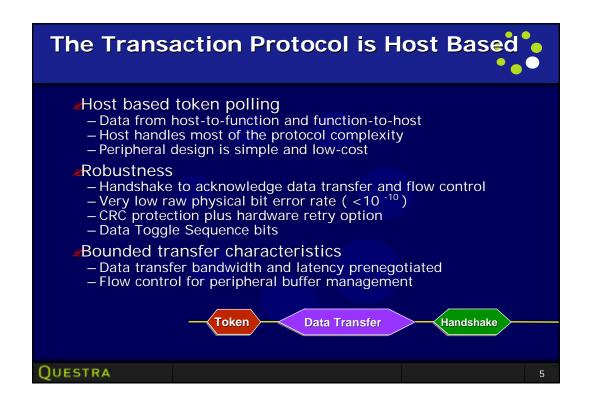


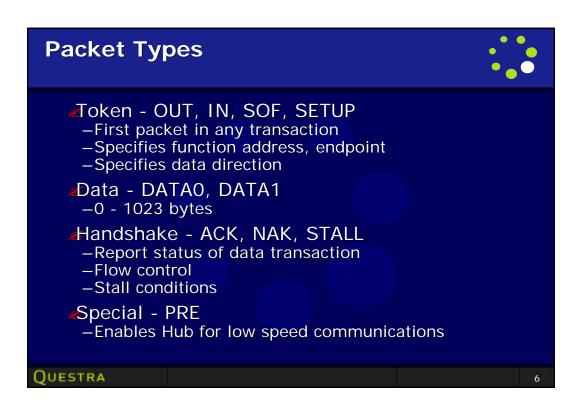
- **JSB Overview**
- **USB** Hardware Controllers
- Architecture of an Embedded USB Device
- **USB** Device Driver Architecture
- Case Study of a USB Device Driver
- ssues to consider
- **C**onclusions

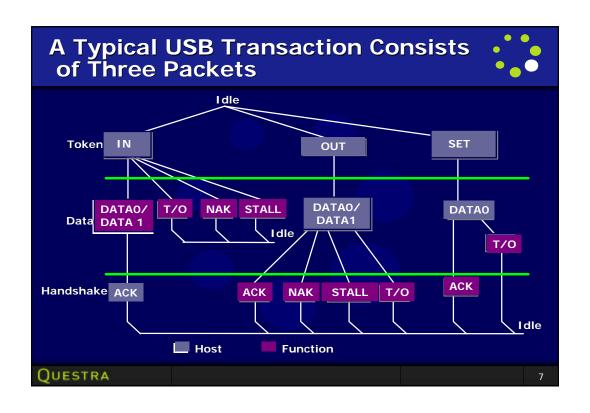
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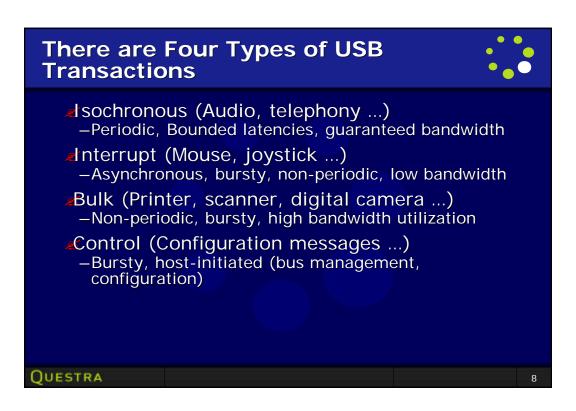
# WSB Overview Hardware Overview Topology Protocol Characteristics Packet Types USB Transactions Enumeration Enumeration Enumeration States Examples of USB devices

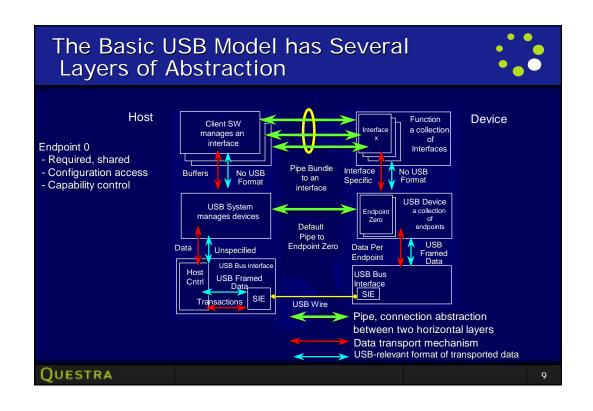


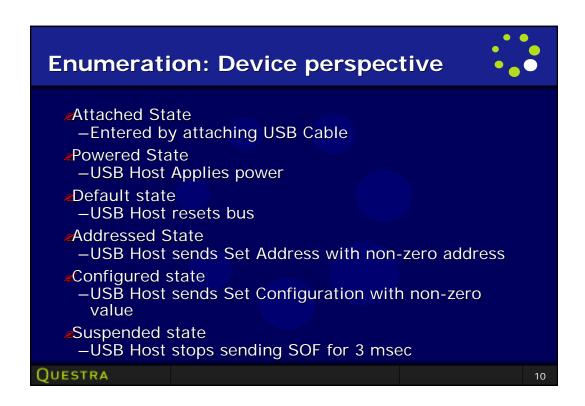


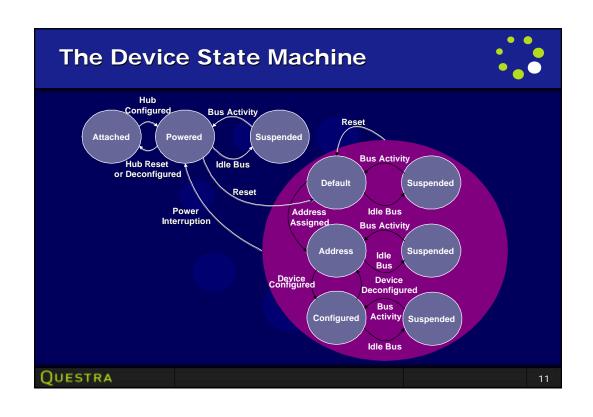


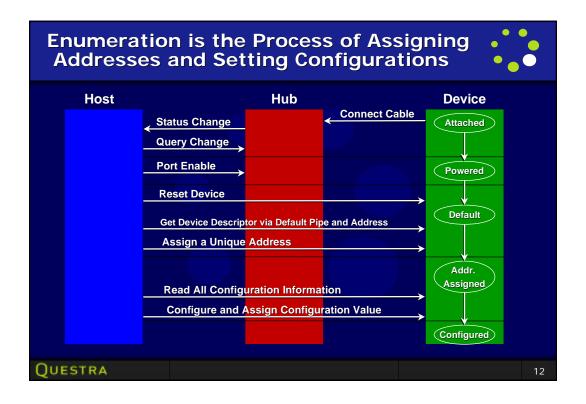














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### **Types of USB Controllers**

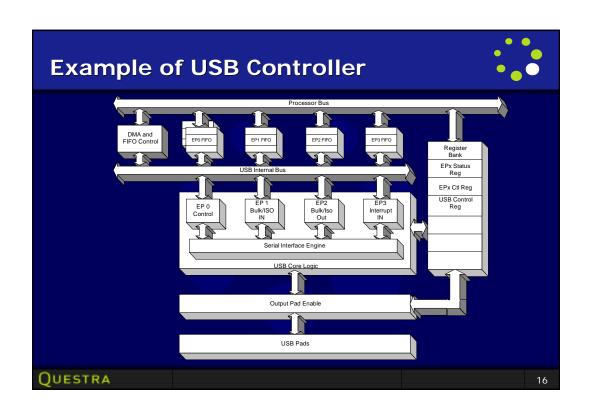


- Discrete Components
  - -NetChip, Intel, National Semiconductor, Phillips
- JSB IP Cores as part of an ASIC
  - -Sand, Motorola, Texas Instruments, .etc
- Combination USB Host and USB Peripheral chip —ScanLogic
- Combination micro-processor and USB Core –8/16 bit processor Mitsubishi, .etc
- Single Chip Solutions
  - -Netchip NET1031 Single chip scanner controller.

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### USB Core Registers for Control and Endpoint Data Transfer FIFO Controller Input and Output FIFOs for Control Endpoint Input or Output FIFO for other Endpoints DMA Controller Internal Bus Serial Interface Engine Output Pads

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### **Key Features of a USB Controller**



### Implements most USB Requests in hardware

- -Standard Requests
  - GET\_DESCRIPTOR and SET\_DESCRIPTOR may be implemented in software for versatility
- -Class/Vendor Requests as appropriate

### **USB** Event Interrupts and status

- Setup, Suspend, Resume, SOF, Reset, Zero Byte Packet
- -DMA Complete
- -Transmit/Receive Ack/Nack/Error status
- -FIFO empty/full or at high/low threshold level

### FIFOs supporting

- -multiple packet depth
- -Hardware Retry of Packet Transfers on error

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### Key Features of a USB Controller II



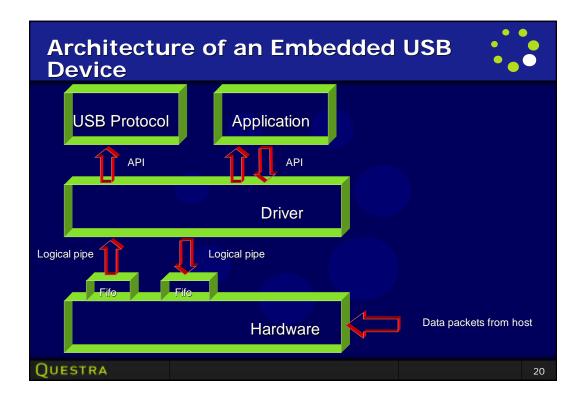
### Hardware should provide ability to

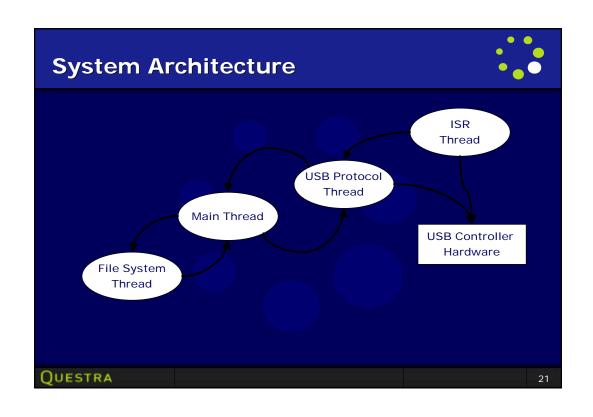
- -initiate a Remote Wakeup
- -detect a USB Reset
- -reset USB Controller
- -Select endpoint as DMA destination
- -Detect enumeration
- Read Current Configuration and Interface
- -Stall endpoints

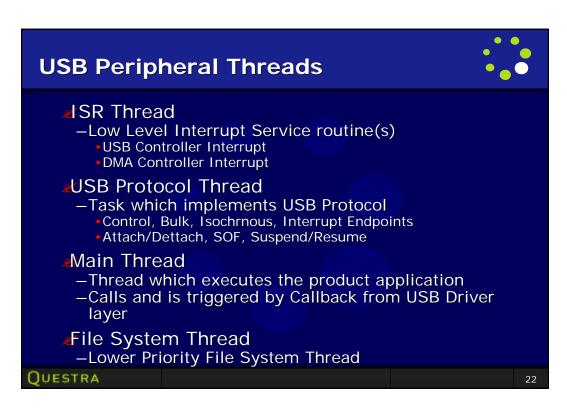
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### Outline USB Overview USB Hardware Controllers Architecture of an Embedded USB Device USB Device Driver Architecture Case Study of a USB Device Driver Testing Strategies Issues to consider Conclusions

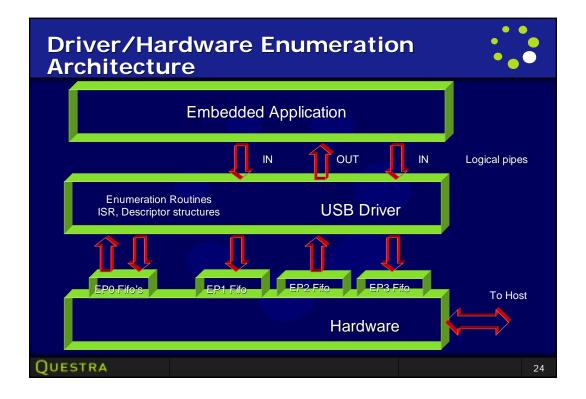
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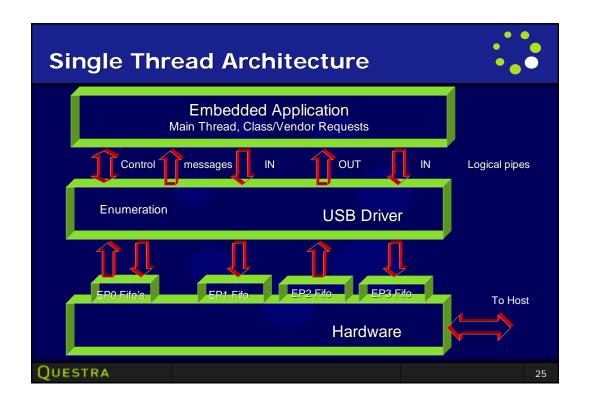


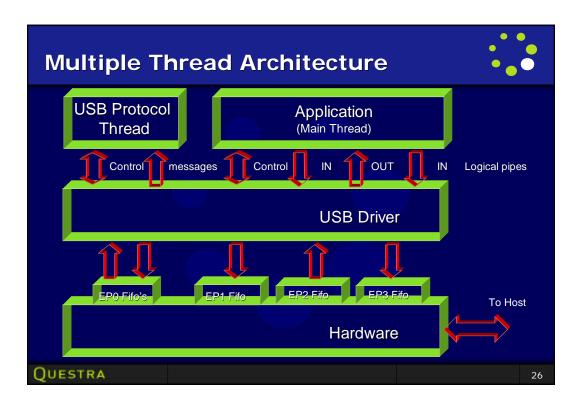




# Interrupt Sources SOF Attach/Dettach Suspend/Resume Setup Packet Data IN Ack Data OUT Ack FIFO Empty or Low level threshold met FIFO Full or High level threshold met DMA Complete







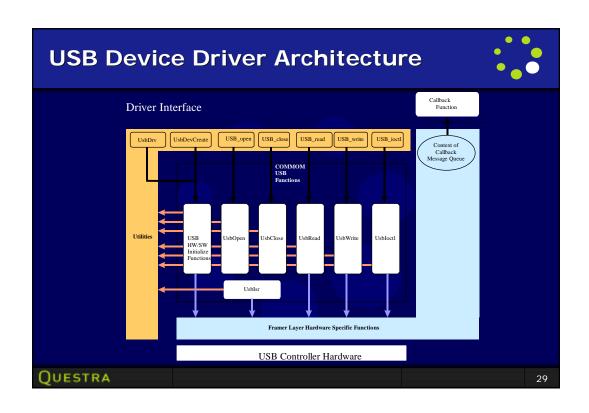


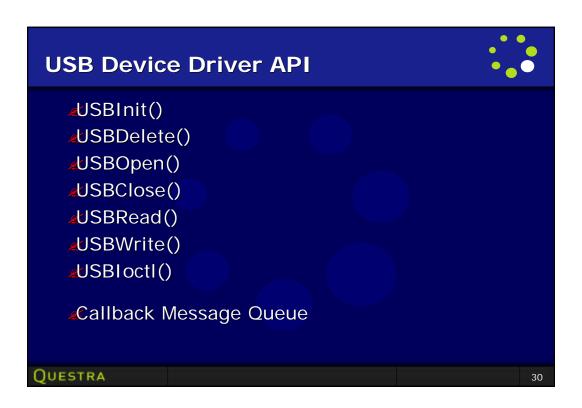
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### **USB Device Driver Components USB Device Driver API Endpoint Data** Control Protocol Attach/ **Status Transfer Protocols Power** - Standard Requests Detach & - Bulk In/Out Management - Class Requests Control Processing - Isochronous In/Out - Vendor Requests - Interrupt In/Out Clock **DMA USB Driver Low Level Access Methods and ISRs Driver Driver** Pull-Up **USB Controller Endpoint FIFO** Clock **DMA** Registers Resistor Registers Registers Controller OUESTRA 28





### USBInit() and USBDelete()



### **USBInit()** - Initialize USB Driver

- –Installs driver in IO system
- Creates or acquires OS resourcesSemaphores, queues, ISR vector, task, memory, etc.
- -Initializes USB Controller hardware
- -Enable USB Controller to allow enumeration

### USBDelete() - Delete USB Driver

- -Disable USB Controller Hardware
- -Return OS resources
  - Semaphores, queues, ISR vector, task, memory, etc.
- –Remove driver from IO system

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### USBOpen() and USBClose()



### ▲USBOpen() - Opens an endpoint

- -If not the Control endpoint
  - Verifies device is enumerated
  - Verifies endpoint is part of current configuration/interface
- -Selects CPU or DMA transfer mode
  - •If DMA selects endpoint's FIFO for use with DMA
- -Set Endpoint states to OPENED

### JSBClose() - Closes an endpoint

- -Disables DMA controller's use of endpoint's FIFO
- –Set endpoint state to CLOSED

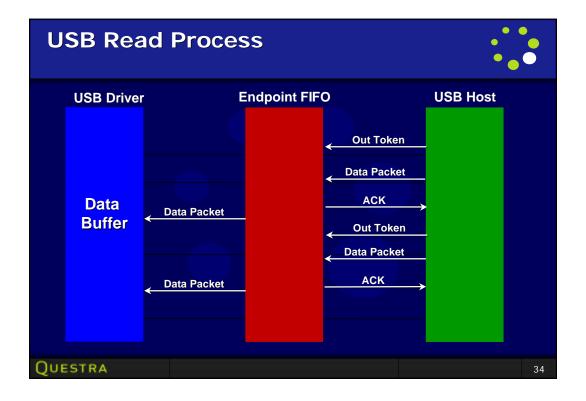
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### USBRead()

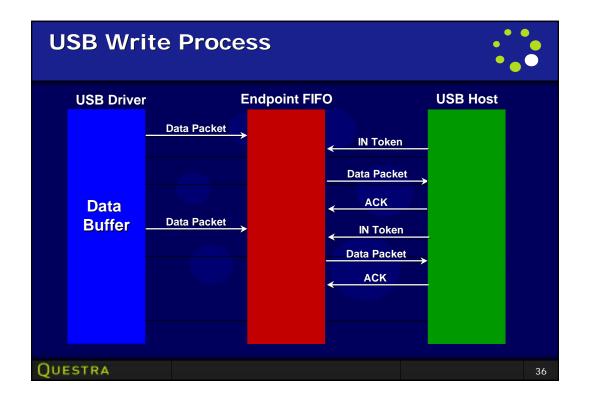


- Read from Bulk, Isochronous or Interrupt endpoint
  - -Verify endpoint is open
  - -If transfer mode is DMA
    - Setup and start DMA read of fixed size from Endpoint FIFO
    - Block until DMA is complete or a timeout occurs
  - -else
    - •ISR Called
    - Loop until all data is read, a timeout occurs or a short packet is received
    - Exit ISR

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# Write to Bulk, Isochronous or Interrupt endpoint Verify endpoint is open If transfer mode is DMA Setup and start DMA write of fixed size to Endpoint FIFO Block until DMA is complete or a timeout occurs else ISR Called Loop until all data is written, or a timeout occurs Exit ISR Questra



### **Control Read**



- Call USBRead( EPO ) to read a Setup Packet
  - -Read from EPO OUT FIFO
- Identify Setup Packet
  - -Standard, Class or Vendor
- Create response to Setup Packet
  - -For example prepare to return a Descriptor
- Call USBWrite (EPO) to write the response
  - -Perform normal USBWrite() function to EPO IN FIFO
  - Wait for Host to return a Zero Byte packet terminating Control transfer
- Repeat

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### **Control Write**



- Call USBRead (EPO) to read a Setup Packet —Read from EPO OUT FIFO
- dentify Setup Packet
  - -Standard, Class or Vendor
- Prepare to receive data from Host
- Call USBRead(EPO) to read data from the Host
  - –Perform normal USBRead() function from EPO OUT FIFO
  - Send a Zero Byte packet to the Host terminating the Control transfer
- Repeat

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### Callback Message Queue



- Message Interface used to send notification to application of asynchronous events
  - -USB Reset
  - -Enumeration
  - -Configuration Change
  - -Interface Change
  - -Suspend/Resume
  - -Attach/Dettach
  - -SOF
  - -Report Setup Packet received by Control Endpoint 0

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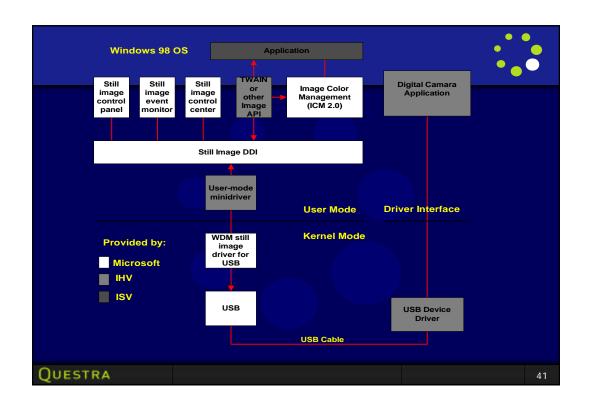
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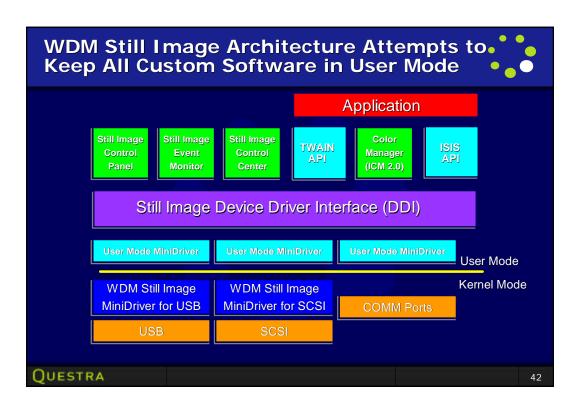
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### **Design Constraints**



- Hardware Selection
  - -Still image architecture requires Control, Bulk In, Bulk Out and Interrupt endpoints.
- Host application controls camera via control or bulk endpoints.
  - Design of the communications protocol is contingent on the Twain data source and any classes supported.
- Host Application defines
  - -Features supported by camera application
  - -Power Management requirements

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### **Effective Testing Strategies**



- Develop Written Test Plans
  - **Define Unit Tests**
  - Define System Tests
- Define minimum USB Host Driver Test
  - Capabilities
  - Enumeration, Data Transfer, Loopback, etc.
- Acquire an USB Analyzer

  - Use analyzer for documenting test results
    Debug Driver enumeration and Data Transfer
    Verify System level behavior with analyzer
    Execute Compliance Test in loop mode (>1000x)
- Purchase a USB Evaluation Board & source code
- **Utilize USB Organizations Test Resources**

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### **Analyzer View** e USB Analyzer - (DATA.USB : CATC Default View) Merueltrisger, LS, roidle Search forward from top af core **OUESTRA** 46

### **Host Software Testing Strategies**



- Schedule availability of Host software
  - -USB mini-driver
  - -Host Test Application
- Request Host Test Application support
  - –Enumeration
  - -Data Transfer
  - -Data Transfer Loopback Testing
  - -Vendor/Class Request Support
- Leverage USB Evaluation sample source
  - -Stimulate USB peripheral using sample code
- System Tests
  - -Perform typical use cases with Product software

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### **Embedded Software Testing Strategies**

### **Unit Tests**

- -Driver Install and Uninstall
- –Enumeration Test
- -Device Driver API
  - Open/Close endpoints
    - Data Transfer (read and write)
    - Select Endpoint using DMA
  - IO Control Test
- –Loopback Testing (>1000x, vary transfer sizes)
- -Vendor/Class Request Support
- System Tests
  - -Perform typical use cases with Product software
  - -System Level Power Management

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### **USB Organization's Testing Resources**

### **USB-IF** Compliance Program

- -Worksheets
  - Device Framework
  - Signal Quality
  - Power Distribution and Consumption
- –Interoperability Guidelines
- -Test Tools
  - USBCheck, HIDView
- -Compliance Workshops
  - Verifies USB Compliance and Interoperability
  - •in-house USB Compliance and Interoperability
    - -Verify throughout product development

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### **USB Analyzers**



### Benefits of an USB Analyzer Tool

- -Passively monitors USB Bus
- -Allows debug of Enumeration, Vendor/Class Requests
- -Reveals system level behavior
- Some tools allow for active introduction of faults, standard Requests or Vendor/Class Requests

### ✓ Drawbacks

- -Purchase Price
- -Selecting which one you want
- Some PC's have demonstrated signal/noise errors with USB analyzer's attached

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### Issues to consider



- More Class Support
  - -HID, Common, Mass Storage, Firmware Upgrade,
- **USB 2.0** 
  - -Do you need it?
  - NOT supported in Windows XP
- **Protocol Stacks**
  - -PIMA/ISO-15740
  - -USB Mass Storage Devices
  - -WDM Still Image Architecture

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### Conclusion



- Selecting a more capable USB controller simplifies the design USB Device Drivers
- Support both CPU and DMA transfers
- Data Transfer Speed is a priority
  - Transfer Data inside ISR

  - Optimize code execution of critical routines
     Design a solution with parallelism of processing and data transfer
  - Dedicate the DMA to the highest bus bandwidth scenarios
- ■USB Compliance testing occurs throughout development
- ∠Take advantage of the state of the sta
  - USB Test tools
  - USB Analyzers
  - Compliance Worksheets

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### **For More Information**



- **∠**USB Specification Rev 1.1, 1.0
- http://www.usb.org the root node
- http://www.intel.com
- www.microsoft.com
  - Search for WDM, WinHEC, ActiveMovie, Still Image, etc.
- **USB System Architecture, Don Anderson - Mindshare Inc.**
- www.linux.org
- www.catc.com
- Questra Corporation (716) 381-0260 www.questra.com

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