


```

SerialOutputString("This is free software, and you are welcome "
                  "to redistribute it \n");
SerialOutputString("under certain conditions; "
                  "read the GNU GPL for details. \n");

```

1. main.c

```

, main()      LED      led_on()      . serial timer
              SerialInit()  TimerInit()      . SerialInit()
              baud9k6(=23)  serial.h      가 .

```

```

typedef enum { /* Some useful SA-1100 baud rates */
    baud1k2 = 191,
    baud9k6 = 23,
    baud19k2 = 11,
    baud38k4 = 5,
    baud57k6 = 3,
    baud115k2 = 1
} eBauds;

```

2. SA1100 Serial baud rate

baud rate

BRD

$$BaudRate = \frac{3.6864 \times 10^6}{16 \times (BRD + 1)}$$

BRD

serial port

UART control register

가 , serial baud rate

serial

가

. serial.c .

```

void SerialInit(eBauds baudrate)
{
#if defined USE_SERIAL1
    while(Ser1UTSR1 & UTSR1_TBY) {
    }
    Ser1UTCR3 = 0x00;
    Ser1UTSR0 = 0xff;

```

```

Ser1UTCR0 = ( UTCR0_1StpBit | UTCR0_8BitData );
Ser1UTCR1 = 0;
Ser1UTCR2 = (u32)baudrate;
Ser1UTCR3 = ( UTCR3_RXE | UTCR3_TXE );
#elif defined USE_SERIAL3
while(Ser3UTSR1 & UTSR1_TBY) {
}
Ser3UTCR3 = 0x00;
Ser3UTSR0 = 0xff;
Ser3UTCR0 = ( UTCR0_1StpBit | UTCR0_8BitData );
Ser3UTCR1 = 0;
Ser3UTCR2 = (u32)baudrate;
Ser3UTCR3 = ( UTCR3_RXE | UTCR3_TXE );
#else
#error "Configuration error: No serial port used at all!"
#endif
}

```

3. SerialInit()

, serial port 1, serial port 3, register set, register set serial port 1 3 가, seial port 가 busy, UTCR3(UART Control Register 3) 0x00, UTSR0(UART Status Register 0) 0xFF, UTCR0 1 stop bit 8 bit data, UTCR1 0, UTCR2 baud rate baud rate, UTCR3 Rx Enable가 Tx Enable bit (set) Rx, Tx가, UTRC1 BRD 4 bit, UTCR2 8 bit 가 1.

TimerInit() time.c, timer interrupt

¹ register ~/include/asm-arm/arch-sa1100/SA-1100.h 가, BLOB compile SA1100 patch가 source 가

```

void TimerInit(void)
{
    /* clear counter */
    OSCR = 0;
    /* we don't want to be interrupted */
    OIER = 0;
    /* wait until OSCR > 0 */
    while(OSCR == 0)
        ;
    /* clear match register 0 */
    OSMR0 = 0;
    /* clear match bit for OSMR0 */
    OSSR = OSSR_M0;
    numOverflows = 0;
}

```

4. TimerInit()

SA1100 OSCR(Operating System Counter Register) up-counter register
4 OSMR(Operating System Match Register)가 . OSCR reset
, OSMR 가 read write . OSCR OSMR
, interrupt enable bit OSSR(Operating System
Status Register) bit , bit interrupt controller bit
route . OSMR3 watchdog match register
, OSCR OSMR3 , SA1100
reset . Reset known state
WMER(Watchdog Match Enable Register)가 , FIQ(Fast Interrupt)
IRQ(Interrupt) interrupt CPU enable , ,
status register clear .
OSCR 0 (clear), OIER(Operating System Timer Enable
Register) 0 . counter interrupt clear .
OSCR countering 가, OSMR0 0
, OSSR OSMR0 bit clear . , OSSR bit 1 write
clear . numOverflows Overflow가 가
0 . timer .
BLOB main() serial string ,

SerialOutputString() ²

serial.c

```
/*
 * Output a single byte to the serial port.
 */
void SerialOutputByte(const char c)
{
#if defined USE_SERIAL1
    /* wait for room in the tx FIFO */
    while((Ser1UTSR0 & UTSR0_TFS) == 0) ;
    Ser1UTDR = c;
#elif defined USE_SERIAL3
    /* wait for room in the tx FIFO */
    while((Ser3UTSR0 & UTSR0_TFS) == 0) ;
    Ser3UTDR = c;
#else
#error "Configuration error: No serial port used at all!"
#endif
    /* If \n, also do \r */
    if(c == '\n')
        SerialOutputByte('\r');
}
/*
 * Write a null terminated string to the serial port.
 */
void SerialOutputString(const char *s) {
    while(*s != 0)
        SerialOutputByte(*s++);
} /* SerialOutputString */
```

5. SerialOutputString()

SerialOutputString() character string , byte

² PACKAGE VERSION
compile

macro BLOB

```

SerialOutputByte()
SerialOutputByte() SerialOutputByte() serial port
    Ser1UTDR Ser3UTDR character
    UTSR , Tx FIFO가 full Tx
FIFO half-full , UTDR 가
“ \n” character “ \r” 가 line feed가
    (recursive)
main()

```

```

    /* get the amount of memory */
    get_memory_map();

    /* initialise status */
    blob_status.kernelSize = 0; /* 0
    . */
    blob_status.kernelType = fromFlash; /* fromFlash kernelType
    . */
    blob_status.ramdiskSize = 0; /* RAM disk 0
    . */
    blob_status.ramdiskType = fromFlash; /* fromFlash ramdiskType
    . */
    blob_status.blockSize = blockSize; /* blockSize
    blockSize(=0x00800000) . */
    blob_status.downloadSpeed = baud115k2; /* Baud rate 11500BPS
    . */

    /* Load kernel and ramdisk from flash to RAM */
    Reload("blob");
    Reload("kernel");
    Reload("ramdisk");

#ifdef BLOB_DEBUG
    /* print some information */
    SerialOutputString("Running from ");
    if(RunningFromInternal())
        SerialOutputString("internal");

```

```

else
    SerialOutputString("external");
SerialOutputString(" flash, blockSize = 0x");
SerialOutputHex(blockSize);
SerialOutputByte(' \n');
#endif

```

6. main.c ()

```

get_memory_map()
RAM disk load , 가
blob_status
main.h blob_status_t ,

```

```

typedef struct {
    int kernelSize; /* */
    block_source_t kernelType; /* 가
*/
    int ramdiskSize; /* RAM disk */
    block_source_t ramdiskType; /* RAM disk 가
. */
    int blobSize; /* BLOB */
    block_source_t blobType; /* BLOB 가
. */
    u32 blockSize; /* Block */
    eBauds downloadSpeed; /* Downloading Speed */
} blob_status_t;

```

7. blob_status_t

```

block_source_t eBauds
main.h serial.h

```

```

typedef enum {
    fromFlash = 0,
    fromDownload = 1
} block_source_t;

```

8. block_source_t

block_source_t 가 , image 가
enumeration fromFlash fromDownload 0 1
blob_status_t , ,
Reload() blob kenrel ramdisk reload . BLOB_DEBUG
debugging SerialOutputXXX()
, BLOB가 internal external 가
RunningFromInternal() inline . inline flash.h
가 .

```
static inline int RunningFromInternal(void) {
    if(((*(u32 *)0xA0000010) & 0x04) == 0)
        return 1;
    else
        return 0;
}
```

9. RunningFromInternal() inline

0xA0000010 0x04 AND 0 1 ,
0 . 0xA0000010 Static Memory Control
Register(MSC0) RBw0 bit 0x04가 AND . ,
bit ROM bus width 1 16 bit , 0 32 bit
bus width 가 . Reset ROM_SEL pin inverse 가
, , ROM , RAM
, get_memory_map() Reload()
memory.c main.c .

```
void get_memory_map(void)
{
    u32 addr;
    int i;

    /* init */
    for(i = 0; i < NUM_MEM_AREAS; i++)
```

```

        memory_map[i].used = 0;
        /* first write a 0 to all memory locations */
        for(addr = MEMORY_START; addr < MEMORY_END; addr +=
TEST_BLOCK_SIZE)
            * (u32 *)addr = 0;
        /* scan memory in blocks */
        i = 0;
        for(addr = MEMORY_START; addr < MEMORY_END; addr +=
TEST_BLOCK_SIZE) {
            if(testram(addr) == 0) {
                /* yes, memory */
                if(* (u32 *)addr != 0) { /* alias? */
#ifdef BLOB_DEBUG
                    /* Debugging          serial          output          . */
#endif
                    if(memory_map[i].used)
                        i++;
                    continue;
                }
                /* not an alias, write the current address */
                * (u32 *)addr = addr;
#ifdef BLOB_DEBUG
                    /* Debugging          serial          output          . */
#endif
                /* does this start a new block? */
                if(memory_map[i].used == 0) {
                    memory_map[i].start = addr;
                    memory_map[i].len = TEST_BLOCK_SIZE;
                    memory_map[i].used = 1;
                } else {
                    memory_map[i].len += TEST_BLOCK_SIZE;
                }
            } else {
                /* no memory here */
                if(memory_map[i].used == 1)
                    i++;
            }
        }

```

```

    }
}

```

10. get_memory_map()

```

    (map)          memory_map[]      used
0   clear        . NUM_MEM_AREAS      가
가          32   가   , memory.h      .   32
alias          . memory_map[]
              memory_area_t
              loop          가
testram()     가   .
              가   ,          save
. return      가   0
              1           .   testmem2.S
              3

```

```

typedef struct {
    u32 start;          /*
    u32 len;            /*
    int used;          /*   flag */
} memory_area_t;

```

11. memory_area_t

```

1MBytes (=TEST_BLOCK_SIZE)   가   block
loop   0   .   (MEMORY_START)
        (MEMORY_END)   0xC0000000   0xE0000000
        SA1100   DRAM BANK 0, 1, 3, 4가
testram()   가 0   , RAM
memory_map[]   block   used   가 1
i   가   ,   loop   ,   가
        block   0   ,   가 alias
        .   memory_map[]   block   used
        1   가   i   가
        ,   (addr)   addr   가   가   , alias

```

³ 가 . testram() .

```

, block (memory_map[i].used == 0), memory_map[]
element , addr , 1
Mbytes(=TEST_BLOCK_SIZE), used 1
(memory_map[i].used == 1), 가 (TEST_BLOCK_SIZE ).
, block block , block

```

```

SerialOutputString("Memory map: \n");
for(i = 0; i < NUM_MEM_AREAS; i++) {
    if(memory_map[i].used) {
        SerialOutputString(" 0x");
        SerialOutputHex(memory_map[i].len);
        SerialOutputString(" @ 0x");
        SerialOutputHex(memory_map[i].start);
        SerialOutputString(" (");
        SerialOutputDec(memory_map[i].len / (1024 * 1024));
        SerialOutputString(" MB) \n");
    }
}
}

```

12. get_memory_map() ()

```

debugging . memory_map[]
element loop | , ,
가 SerialOutputHex() SerialOutputDec() . serial.c

```

```

void SerialOutputHex(const u32 h)
{
    char c;
    int i;

    for(i = NIBBLES_PER_WORD - 1; i >= 0; i--) { /* NIBBLES_PER_WORD =
8 */
        c = (char)((h >> (i * 4)) & 0x0f);
        if(c > 9)

```

```

        c += ('A' - 10);
    else
        c += '0';
    SerialOutputByte(c);
}
}
void SerialOutputDec(const u32 d)
{
    int leading_zero = 1;
    u32 divisor, result, remainder;

    remainder = d;
    for(divisor = 1000000000;
        divisor > 0;
        divisor /= 10) {
        result = remainder / divisor;
        remainder %= divisor;
        if(result != 0 || divisor == 1)
            leading_zero = 0;
        if(leading_zero == 0)
            SerialOutputByte((char)(result) + '0');
    }
}

```

13. SerialOutputHex() SerialOutputDec()

	32 bit	hexadecimal	decimal	serial
	,	SerialOutputByte()		
byte	.	data	conversion	.

```

void Reload(char *commandline)
{
    u32 *src = 0;
    u32 *dst = 0;
    int numWords;

    if(MyStrNCmp(commandline, "blob", 4) == 0) {

```

```

        src = (u32 *)BLOB_RAM_BASE;
        dst = (u32 *)BLOB_START;
        numWords = BLOB_LEN / 4;
        blob_status.blobSize = 0;
        blob_status.blobType = fromFlash;
        SerialOutputString("Loading blob from flash ");
    } else if(MyStrNCmp(commandline, "kernel", 6) == 0) {
        src = (u32 *)KERNEL_RAM_BASE;
        dst = (u32 *)KERNEL_START;
        numWords = KERNEL_LEN / 4;
        blob_status.kernelSize = 0;
        blob_status.kernelType = fromFlash;
        SerialOutputString("Loading kernel from flash ");
    } else if(MyStrNCmp(commandline, "ramdisk", 7) == 0) {
        src = (u32 *)RAMDISK_RAM_BASE;
        dst = (u32 *)INITRD_START;
        numWords = INITRD_LEN / 4;
        blob_status.ramdiskSize = 0;
        blob_status.ramdiskType = fromFlash;
        SerialOutputString("Loading ramdisk from flash ");
    } else {
        SerialOutputString("*** Don't know how to reload \n");
        SerialOutputString(commandline);
        SerialOutputString("\n\n");
        return;
    }
    MyMemCpy(src, dst, numWords);
    SerialOutputString(" done \n");
}

```

14. Reload()

Reload()	commandline	string	.	MyStrCmp()
		, blob kernel, ramdisk	,	
		. MyStrCmp()		
		가	. MyStrCmp()	util.c
	MyMemCpy()		.	

```

int MyStrNCmp(const char *s1, const char *s2, int maxlen)
{
    int i;

    for(i = 0; i < maxlen; i++) {
        if(s1[i] != s2[i])
            return ((int) s1[i]) - ((int) s2[i]);
        if(s1[i] == 0)
            return 0;
    }
    return 0;
} /* MyStrNCmp */

```

15. MyStrCmp()

MyStrNCmp()

0
string index

```

void MyMemCpy(u32 *dest, const u32 *src, int numWords)
{
#ifdef BLOB_DEBUG
    SerialOutputString("\n### Now copying 0x");
    SerialOutputHex(numWords);
    SerialOutputString(" words from 0x");
    SerialOutputHex((int)src);
    SerialOutputString(" to 0x");
    SerialOutputHex((int)dest);
    SerialOutputByte(' \n');
#endif
    while(numWords --) {
        if((numWords & 0xffff) == 0x0)
            SerialOutputByte('.');
        *dest++ = *src++;
    }
#ifdef BLOB_DEBUG

```

```

SerialOutputString(" done \n");
#endif
} /* MyMemCpy */

```

16. MyMemCpy()

```

MyMemCpy()
    (dest)
    (src)
    32 bit loop (copy)
    가 0xFFFF AND 0 “.” serial 가
    가 , if MyStrNComp() . 가
    “blob” . , src dst
    BLOB_RAM_BASE(=0xC1000000) BLOB_START(=0x00000000) 가 .4
    numWords BLOB_LEN(=0x10000 or 64K) 4 ,
    blob_status.blobSize 0 blobType fromFlash(=0 or download from flash)
    . , loading serial “loading
    blob from flash” . MyMemCpy() dst src가 가
    copy .
    commandline “kernel” src
    KERNEL_RAM_BASE(=0xC0008000)5 , dst KERNEL_START(=0x10000 or
    64K) . numWords KERNEL_LEN(=0xC0000 or 3 x
    256K) 4 4 byte ,
    blob_status.kenelSize 0 blob_status.kernelType fromFlash
    flash loading . loading
    SerialOutputString() serial output . copy
    MyMemCpy()가 .

```

⁴ Assabet 가 flash.h

⁵ compile 0xC0008000