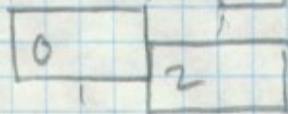
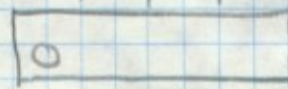


bytes



16-bit words

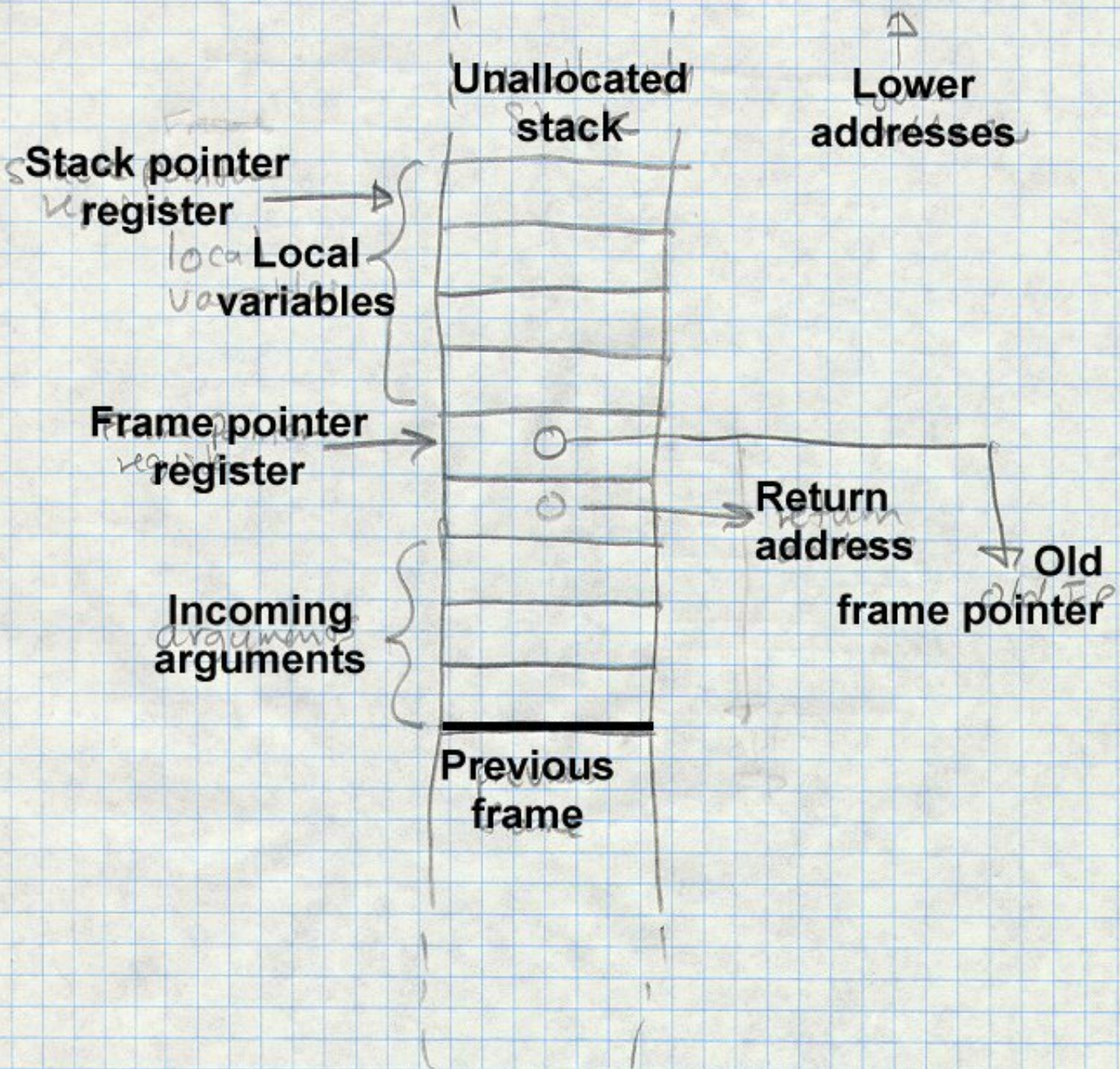


32-bit words

big-endian



little-endian



bytes

1

2

3

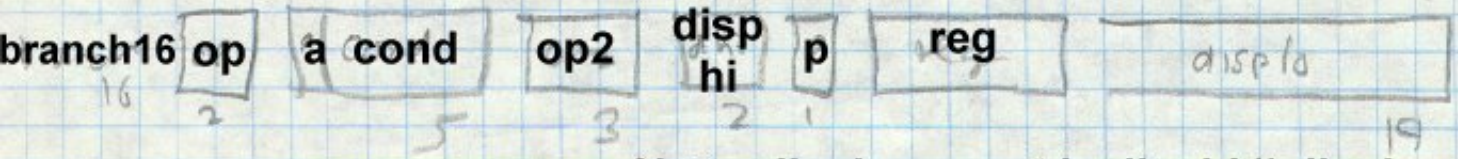
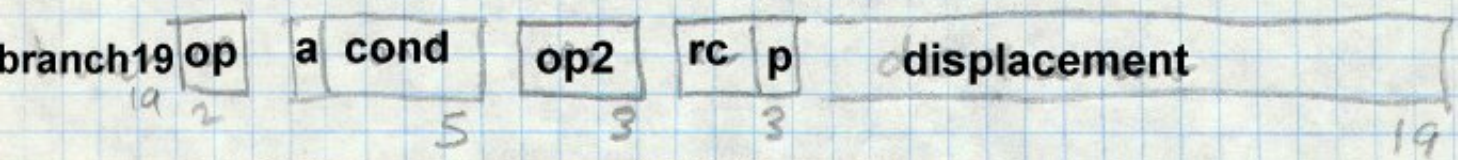
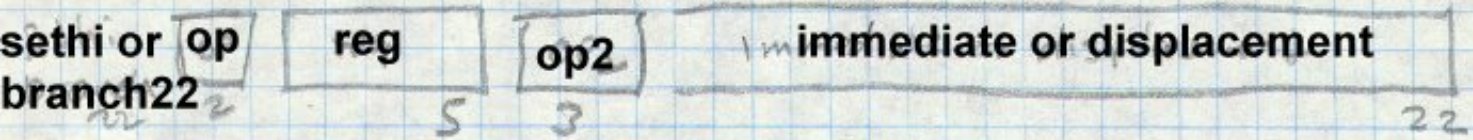
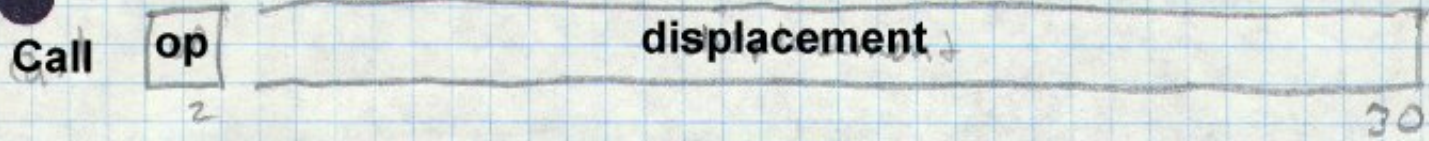
4

5

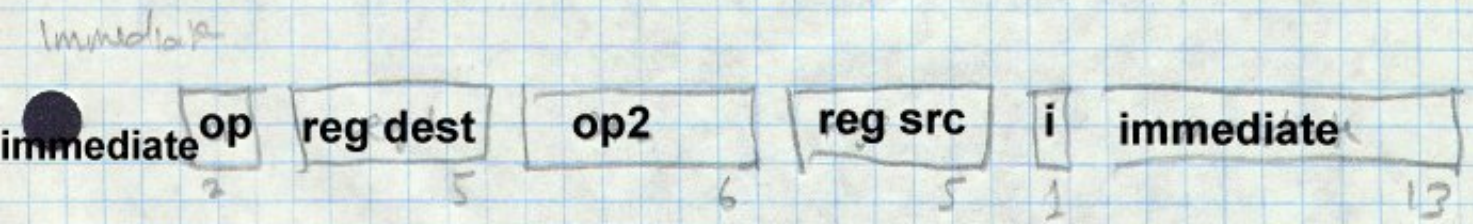
6

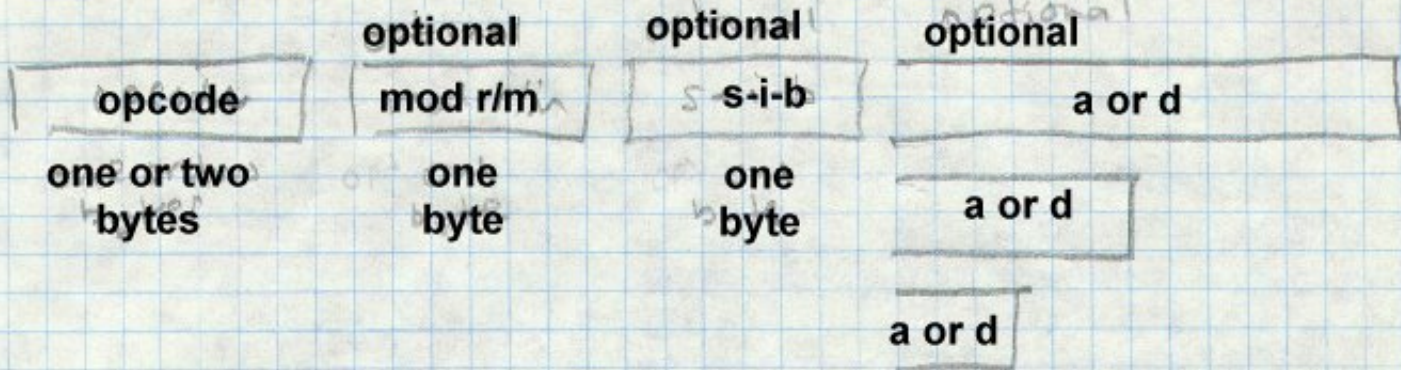
Note to artist: In all letter-digit pairs the digit should be a subscript, like R_2

RR	OP	R1 R2				
RX	OP	R1 X2	B2		D2	
RS	OP	R1 R3	B2		D2	
SI	OP	I2	B1		D1	
SS	OP	L1 L2	B1		D1	B2 D2



Note: displacement is disphi || displa





One, two, or four byte address or displacement

Mod r/m specifies address format

S-I-B specifies scaled index and/or base register

Address may be absolute or relative to base and/or index

base and

Virtual
Virtual
address
space

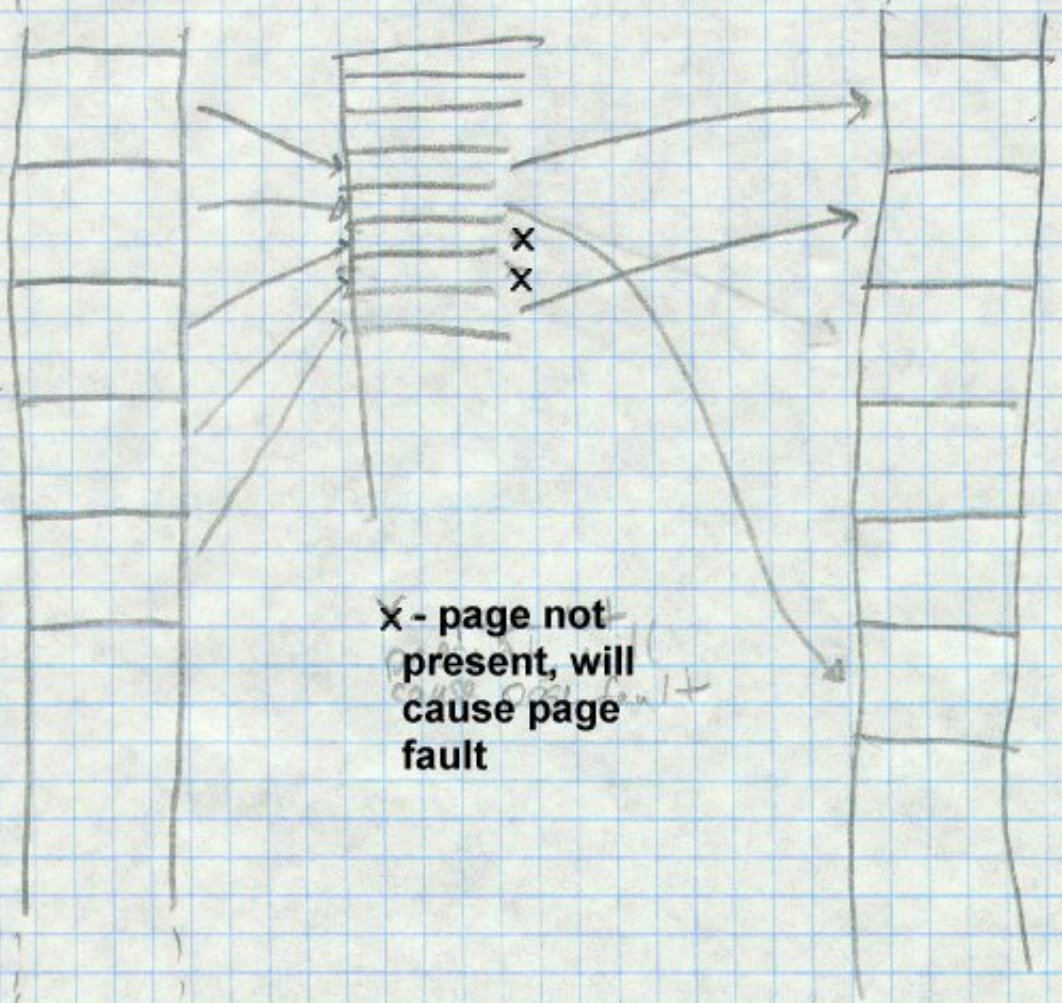
Page table
Page table

Physical
Physical
memory

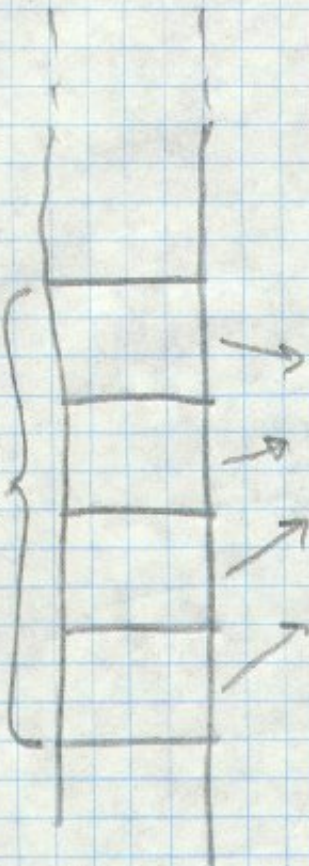
4K or
8K page

X
X

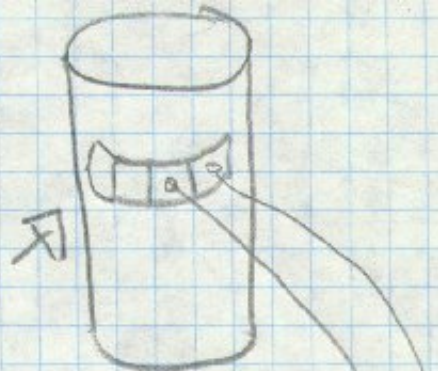
X - page not
present, will
cause page
fault



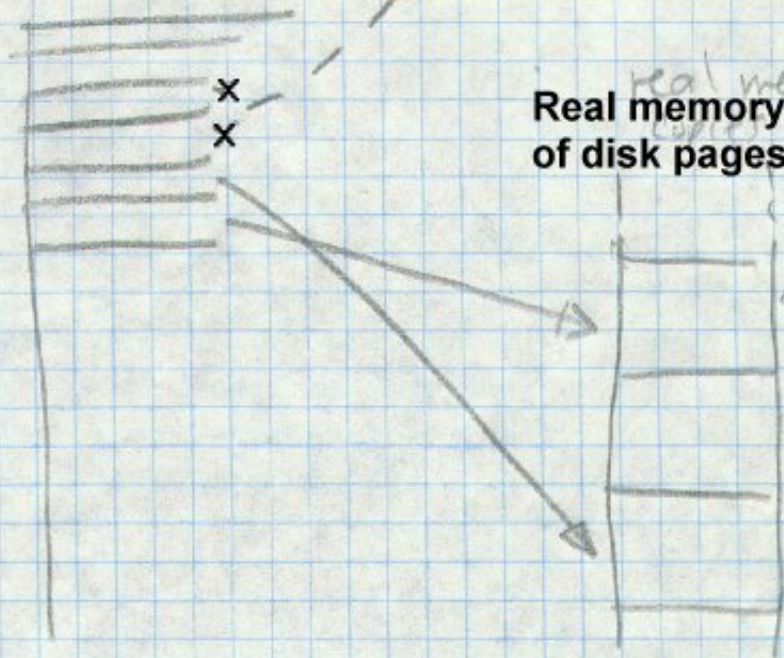
Mapped
address
range



disk
file



Real memory
copies
of disk pages



x - not present, will be mapped from file on reference