

USB Media Access Controller

FEATURES

- Mixed Voltage Support
 - Supports 3.3V Operation
 - Supports Mixed Internal 3.3V Operation with 3.3V/5V External Configuration (5V Tolerant/TTL Compliant IO)
- Complete USB 1.1 Specification Compatibility
 - High Speed Upstream USB Transceivers
 - Serial Interface Engine
 - Autonomous USB Protocol Handling
 - One Bi-directional Control Endpoint with 64 Byte Buffers
 - Two IN and Two OUT Bulk/Interrupt Endpoints with 64 Byte Buffers, One Set with DMA Capability
 - All Endpoint Buffers are Double Buffered
 - Error Status Register for System Quality Monitoring

- Programmable Multi-Protocol Host Interface
 - ISA-Style 5-Bit Address and 16/8-Bit Data Bus
 - IOCHRDY and No Wait State Support for Fast Accesses
 - Non-ISA 16/8-Bit Multiplexed Address/Data Bus
 - Programmable Read/Write Interface
 - Two 16/8-Bit DMA Channels
 - One Programmable IRQ
 - Chip Select
 - Multihost Interface Support Includes Hitachi and Mitsubishi Microcontrollers
- 24MHz Crystal Oscillator
 - Supports Internal or External Clock Source
- 48 Pin TQFP Package



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